UTC DTA144T

PNP DIGITAL TRANSISTOR

PNP DIGITAL TRANSISTOR (BUILT-IN RESISTOR)

FEATURES

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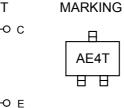
*Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.

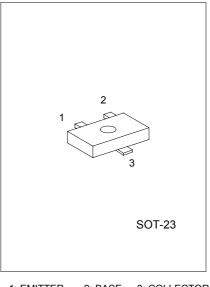
*The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input They also have the advantage of almost completely eliminating parasitic effects.

*Only the on / off conditions need to be set for operation, making device design easy.

EQUIVALENT CIRCUIT

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1: EMITTER 2: BASE 3: COLLECTOR

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL RATINGS		UNIT	
Collector-base voltage	Vсво	-50	V	
Collector-emitter voltage	VCEO	-50	V	
Emitter-base voltage	Vebo	-5	V	
Collector current	lc	-100	mA	
Collector Power dissipation	Pc	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55~+150	°C	

ELECTRICAL CHARACTERISTICS (Ta=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-base breakdown voltage	ВУсво	Ic=-50μA	-50			V
Collector-emitter breakdown voltage	BVCEO	Ic=-1mA	-50			V
Emitter-base breakdown voltage	BV EBO	Iε=-50μA	-5			V
Collector cutoff current	Ісво	Vcb=-20V			-0.5	μA
Emitter cutoff current	Іево	VEB=-4V			-0.5	μA
Collector-emitter saturation voltage	VCE(sat)	Ic=-5mA, Iв= -0.5mA			-0.3	V
DC current transfer ratio	hfe	Vce=-5V, Ic= -1mA	100	250	600	
Input resistance	R1		32.9	47	61.1	kΩ
Transition frequency	fr	Vce=-10V, Ie=5mA, f=100MHz *		250		MHz

* Transition frequency of the device

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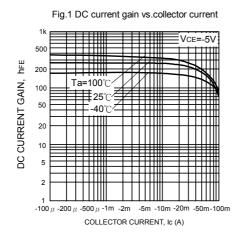
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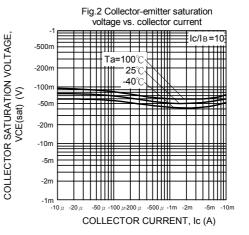
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ELECTRICAL CHARACTERISTIC CURVES





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